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Pss8050 npn medium power 25 v transistor

NPN medium power 25 V transistor PSS8050 (IEC 60134). Notes 1. Cc collector capacitance VCB = 10V I E = i e = 0; f = 1 MHz 10 pF.

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74lvc16374a; 74lvch16374a 16-bit edge-triggered

High-impedance outputs when VCC = 0 V Complies with JEDEC standard JESD8-B plastic thermal enhanced extremely Fig 1. Logic symbol Fig 2. IEC logic

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Pemb15; pumb15 pnp/pnp resistor-equipped

(IEC 60134). Symbol Parameter Conditions Min Max Unit Per transistor B A y 0 1 2 mm scale c X 132 6 5 4 Plastic surface mounted package; 6 leads SOT363 UNIT

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